

| Ref # | Hits | Search Query | DBs | Default Operator | Plurals | Time Stamp |
|-------|------|--|--|------------------|---------|------------------|
| L1 | 943 | (data adj bus) and (parasitic adj capacitance) | USPAT; USOCR; EPO; JPO; DERWENT | OR | OFF | 2005/03/05 15:26 |
| L2 | 145 | 1 and (memory adj access) | USPAT; USOCR; EPO; JPO; DERWENT | OR | OFF | 2005/03/05 15:26 |
| L3 | 23 | 2 and decoupl\$3 | USPAT; USOCR; EPO; JPO; DERWENT | OR | OFF | 2005/03/05 15:27 |
| L4 | 109 | 2 and switch | USPAT; USOCR; EPO; JPO; DERWENT | OR | OFF | 2005/03/05 15:27 |
| L5 | 72 | 4 and DRAM | USPAT; USOCR; EPO; JPO; DERWENT | OR | ON | 2005/03/05 15:30 |
| L6 | 1561 | (data adj bus) and (synchronous adj DRAM) | USPAT; USOCR; EPO; JPO; DERWENT | OR | ON | 2005/03/05 15:31 |
| L7 | 60 | 6 and (parasitic adj capacitance) | USPAT; USOCR; EPO; JPO; DERWENT | OR | ON | 2005/03/05 15:31 |
| L8 | 0 | 7 and rduc\$3 | USPAT; USOCR; EPO; JPO; DERWENT | OR | ON | 2005/03/05 15:31 |
| L9 | 57 | 7 and reduc\$3 | USPAT; USOCR; EPO; JPO; DERWENT | OR | ON | 2005/03/05 15:32 |